

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	("timing resistor" AND "timing capacitor" AND "dead time" AND frequency).CLM.	US-PGPUB; USPAT	OR	ON	2006/08/02 15:16
L2	1	("secondary bias circuit" AND "primary open loop" AND rectification AND filtering).CLM.	US-PGPUB; USPAT	OR	ON	2006/08/02 15:15